

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Jon Scott Miller et al. : Art Unit:
Serial No.: To Be Assigned : Examiner:
Filed: Herewith :
FOR: METHOD OF DECREASING DELAY THROUGH :
FRAME BASED FORMAT CONVERTERS :

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

S I R :

Prior to examination, please amend the above-identified application as follows:

IN THE CLAIMS:

Please replace claim 12 with the following amended claim:

1 12. (Amended) A method in accordance with claim 11, wherein the steps
2 of converting the active video top half field and of converting the active video bottom half
3 field each includes concurrently processing a predetermined number of lines of the respective
4 top half field and bottom half field, whereby the step of converting defines a predetermined
5 number of phases, wherein the step of converting the active video bottom half field further
6 includes the step of adjusting the phase with which the bottom half field is converted to
7 match an ending processing phase of the top half field.

Respectfully Submitted,



Kenneth N. Nigon, Reg. No. 31,549
Attorney for Applicants

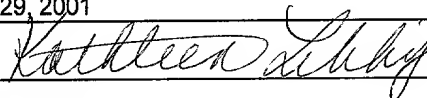
Dated: March 29, 2001

Suite 301
One Westlakes, Berwyn
P.O. Box 980
Valley Forge, PA 19482-0980
(610) 407-0700

The Assistant Commissioner for Patents is hereby authorized to charge payment to Deposit Account No. 18-0350 of any fees associated with this communication.

I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail in an envelope with sufficient postage addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on:

March 29, 2001



Kathleen Libby

VERSION WITH MARKINGS TO SHOW CHANGES MADE

- 1 12. (Amended) A method in accordance with claim-~~10~~11, wherein the steps of
2 converting the active video top half field and of converting the active video bottom half
3 field each includes concurrently processing a predetermined number of lines of the
4 respective top half field and bottom half field, whereby the step of converting defines a
5 predetermined number of phases, wherein the step of converting the active video
6 bottom half field further includes the step of adjusting the phase with which the bottom
7 half field is converted to match an ending processing phase of the top half field.